

FIG. 1

FIG. 2A

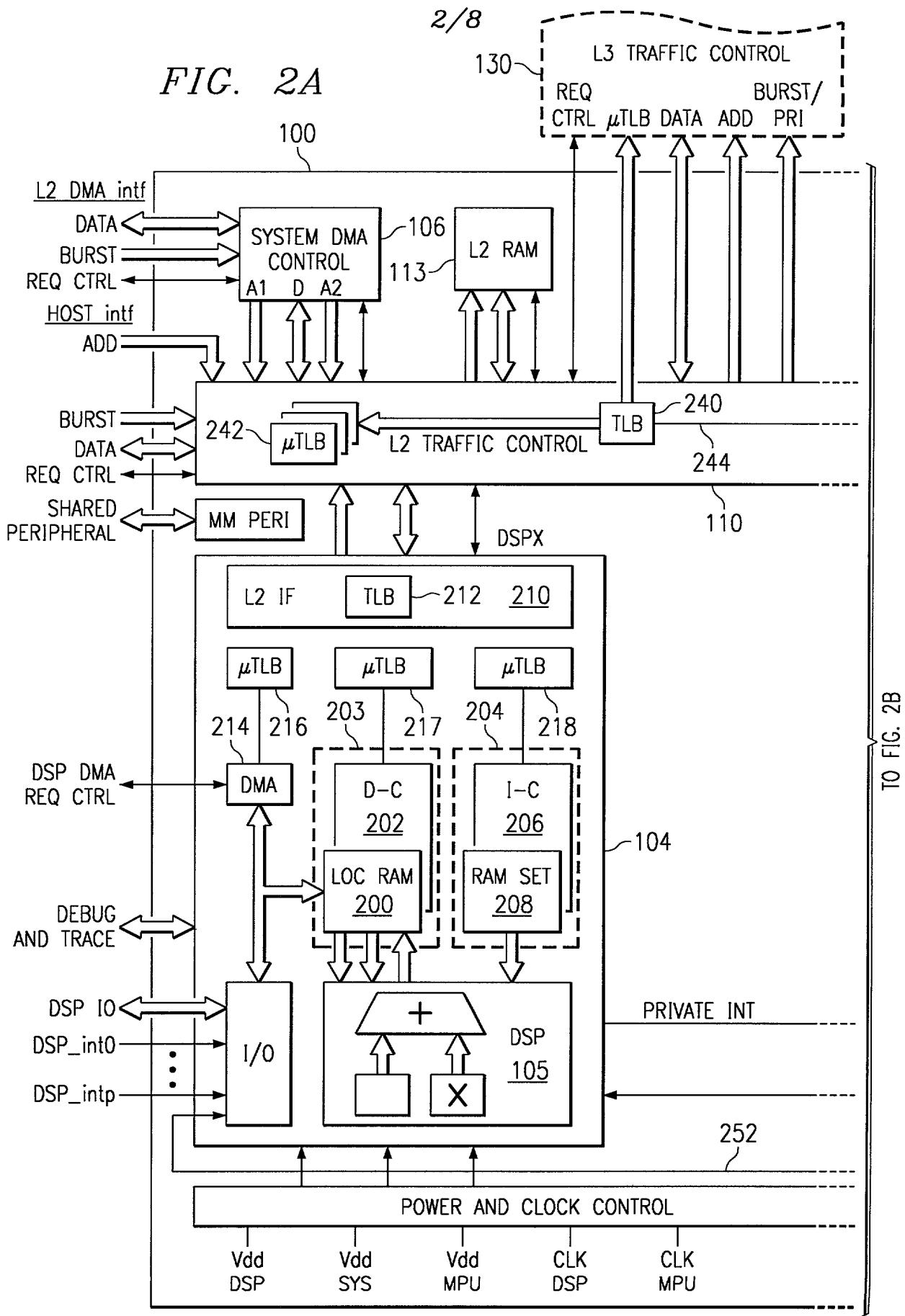
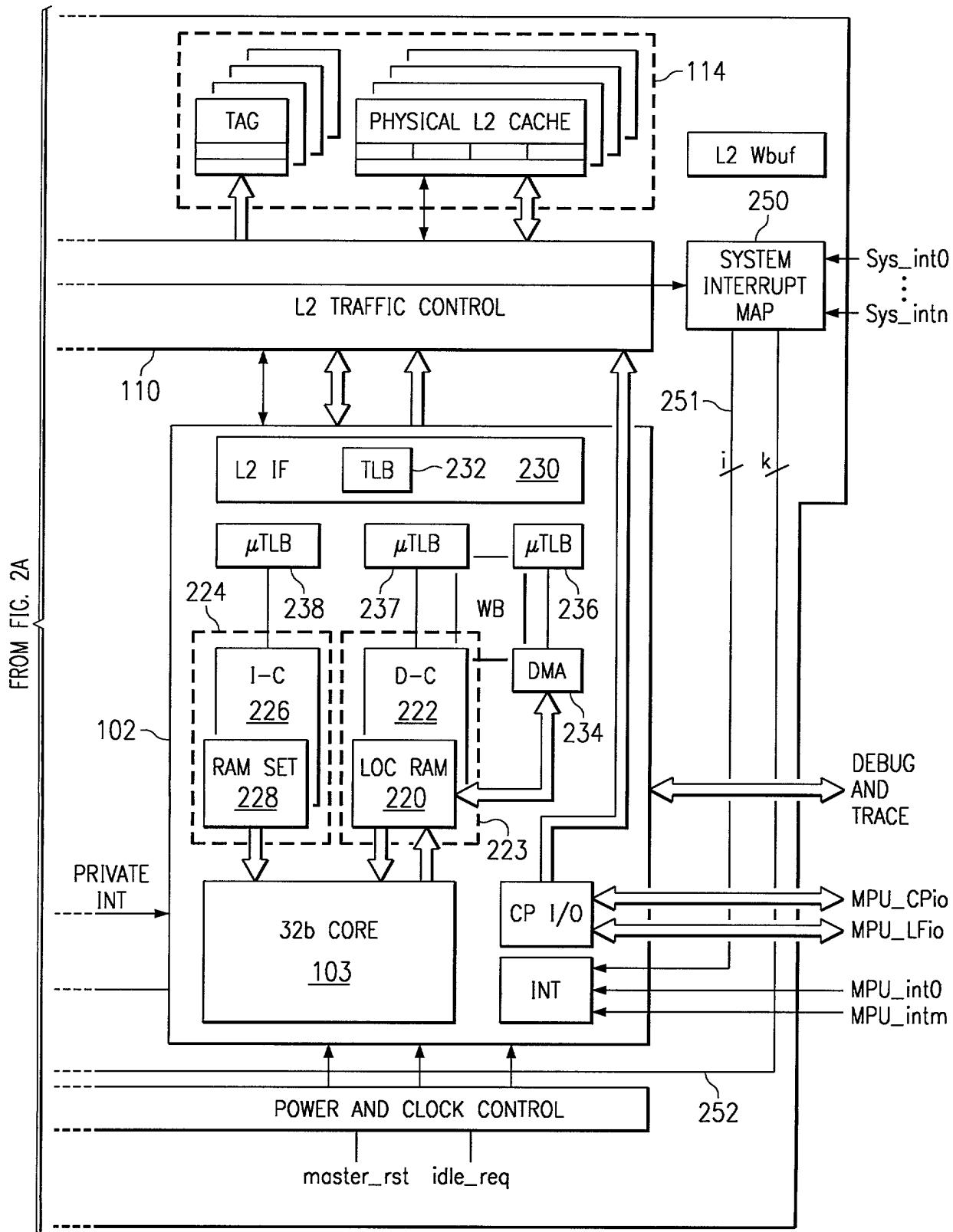


FIG. 2B



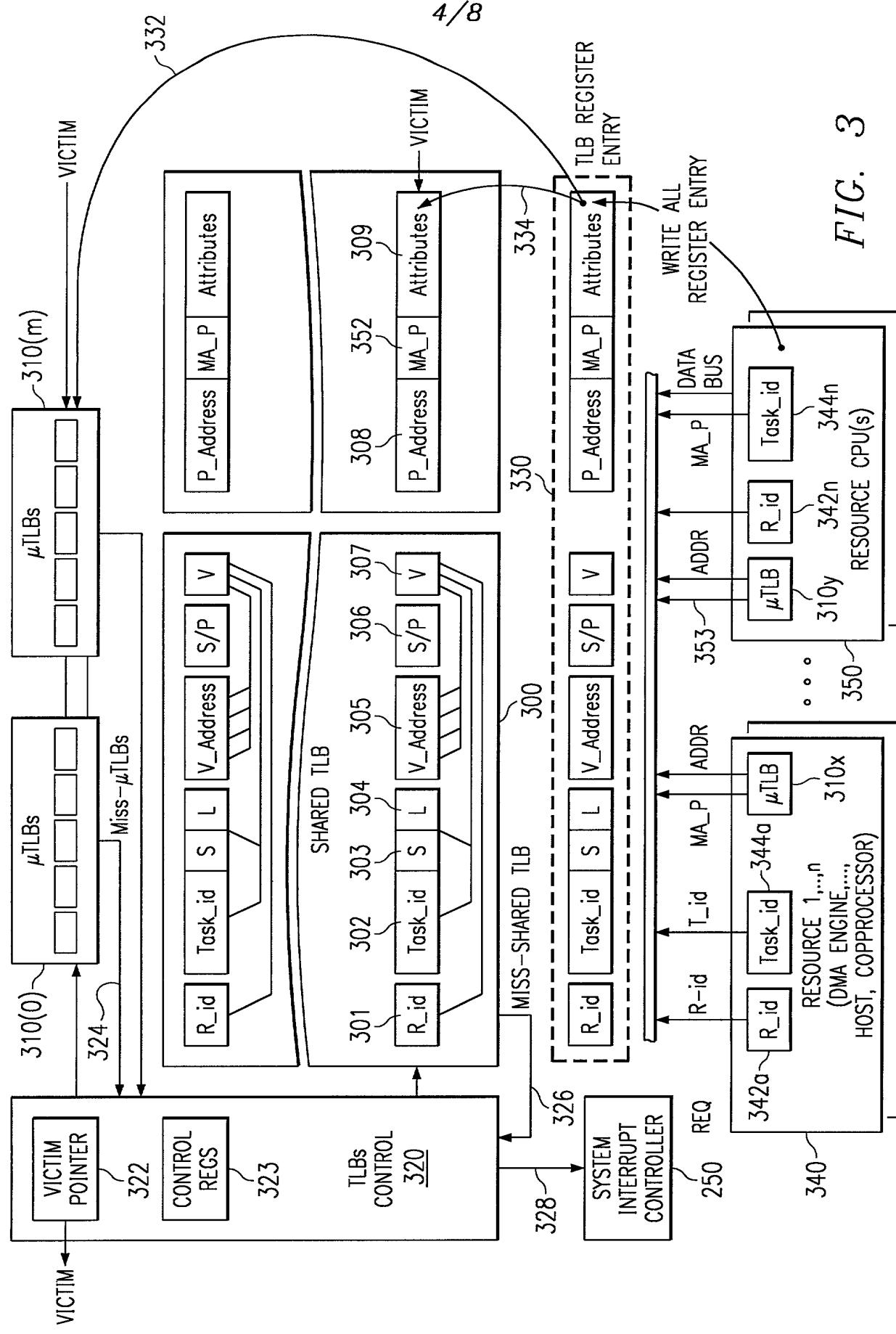


FIG. 3

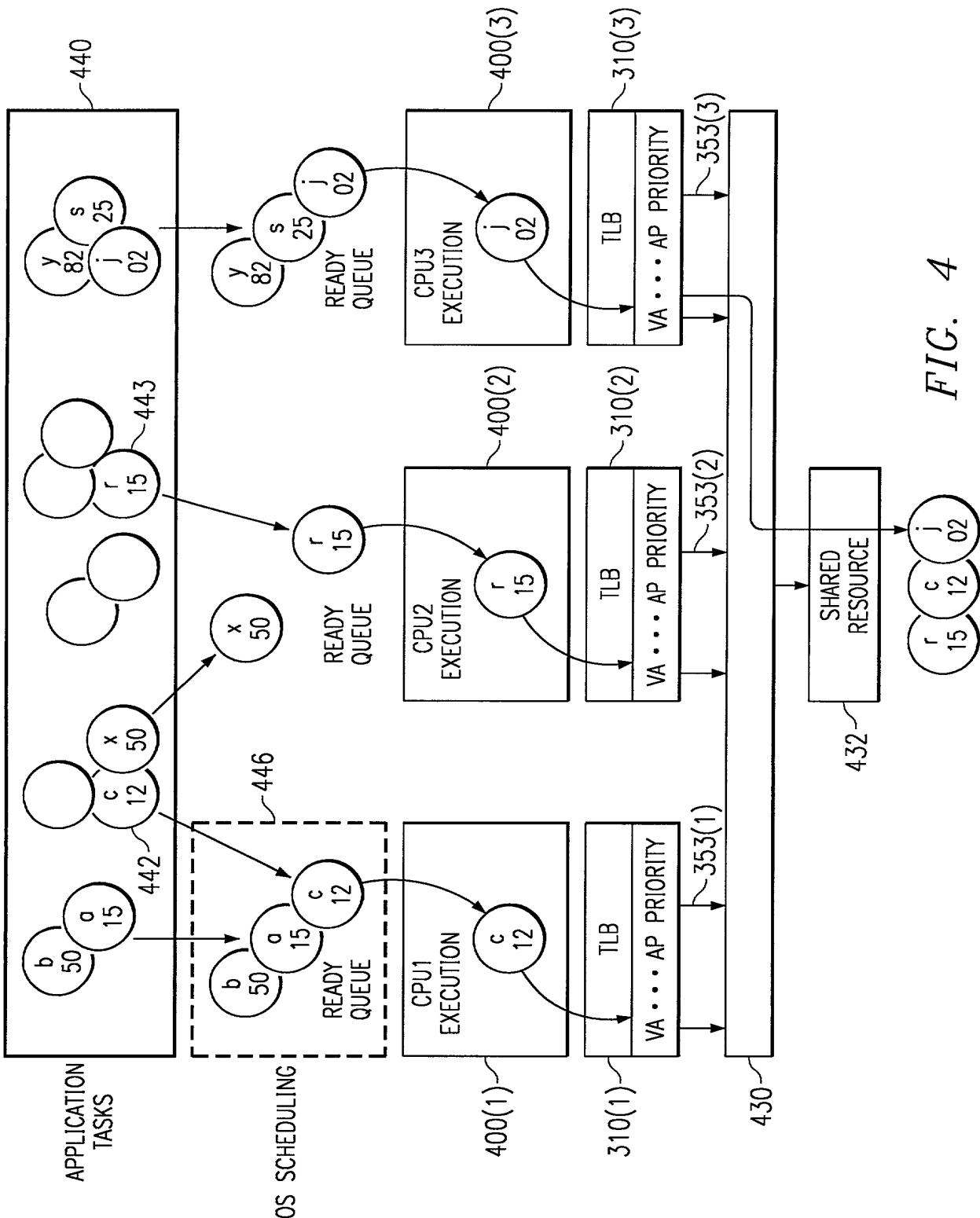


FIG. 4

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400(1)~CPU1	c.12		a.15		a.15		a.15	
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400(2)~CPU2	r.15		r.15		x.50			
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400(3)~CPU3	j.02		j.02		s.25			
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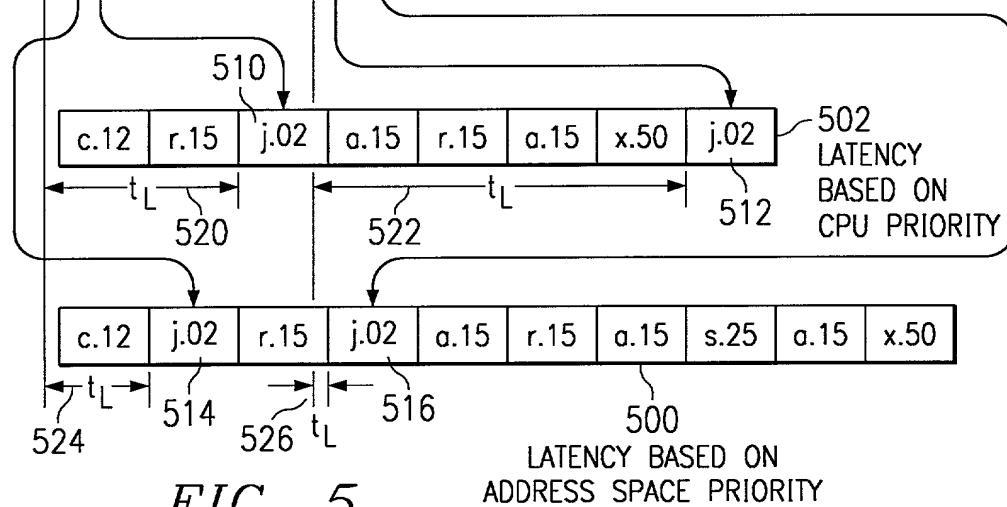


FIG. 5

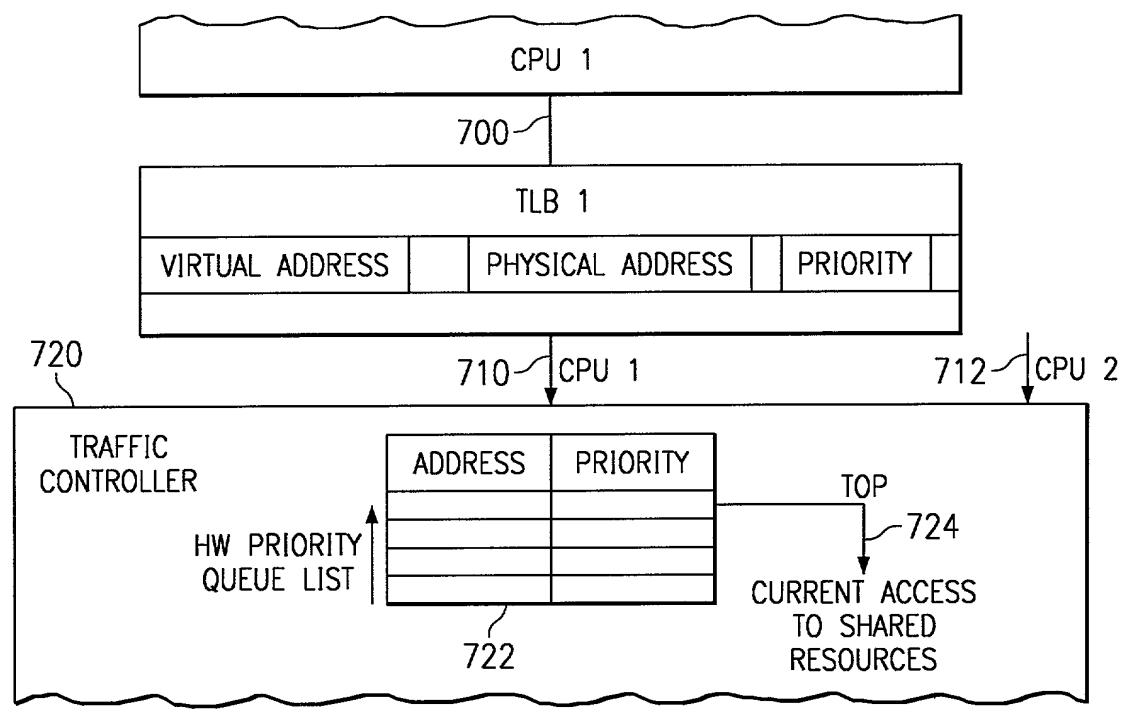
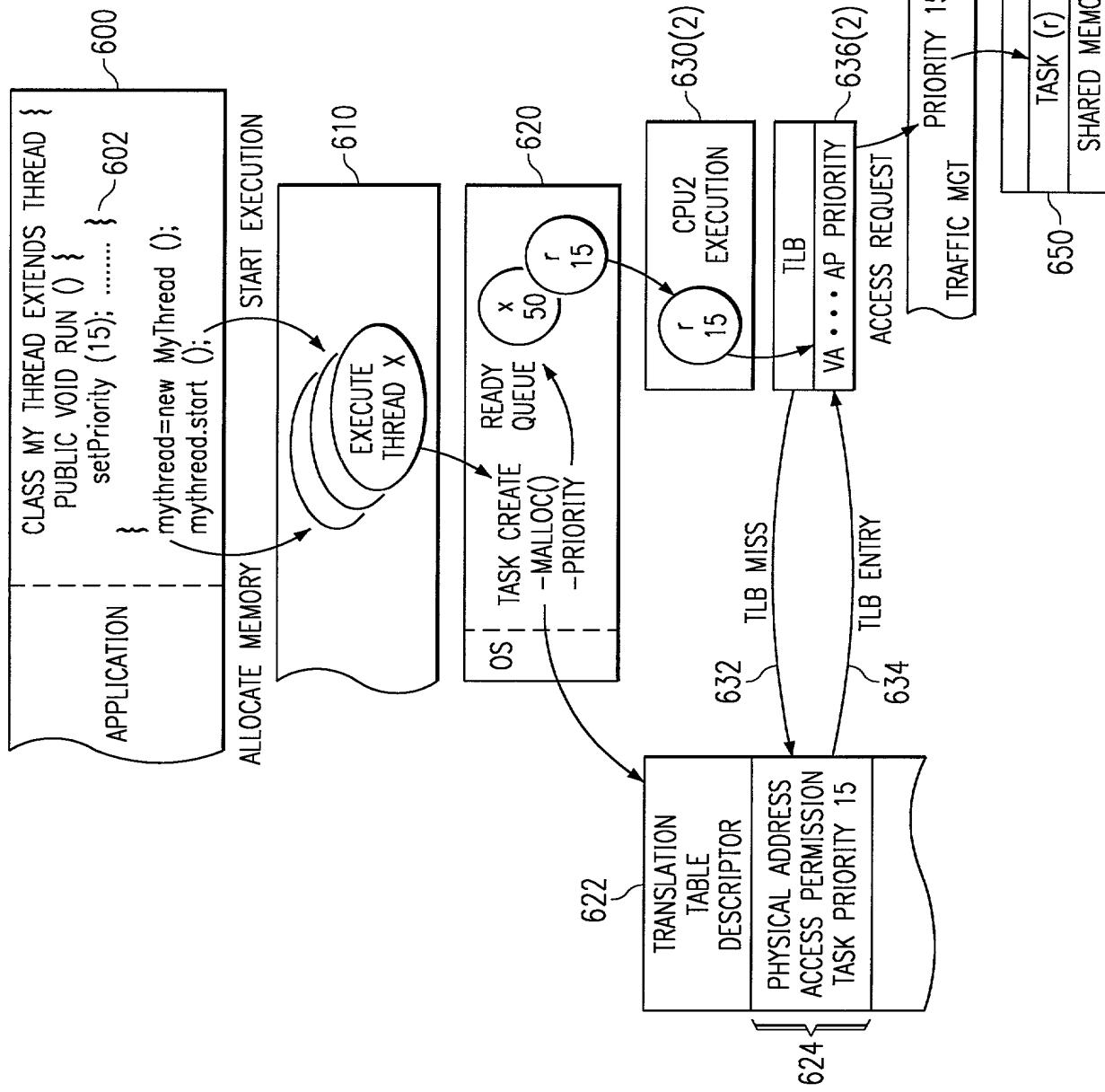


FIG. 7

FIG. 6



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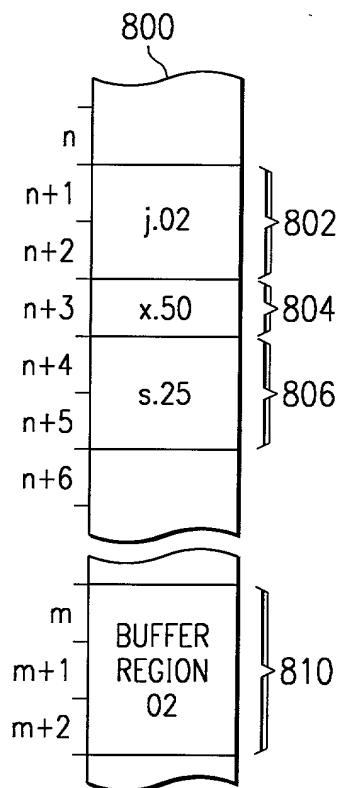


FIG. 8

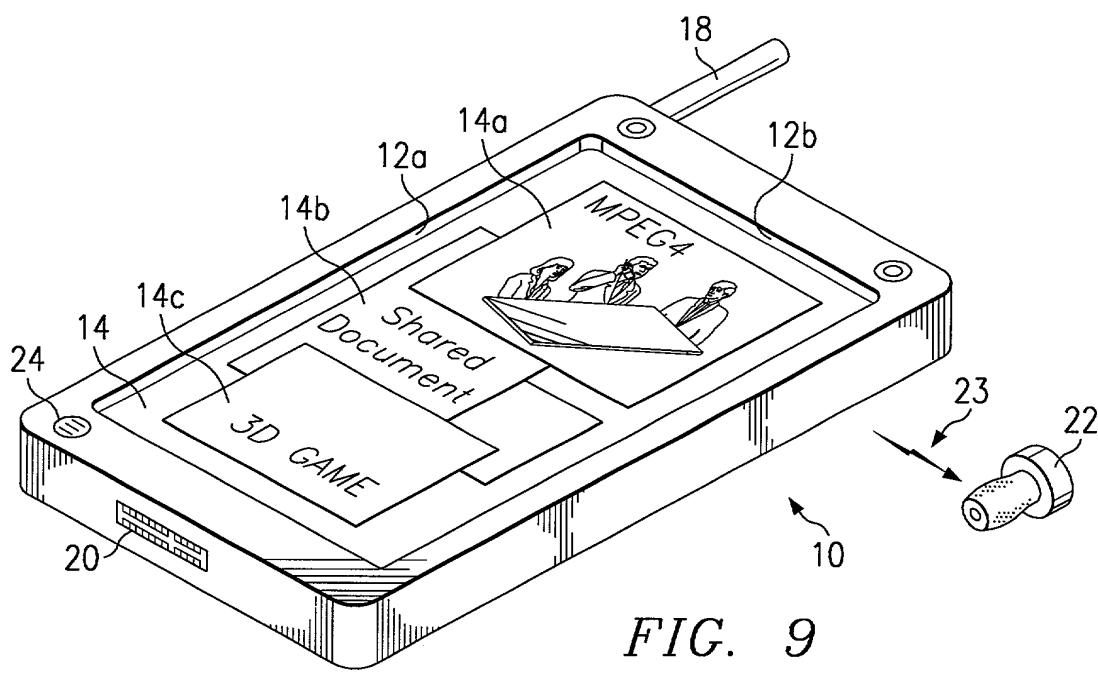


FIG. 9